

Module Code	TF-3301		
Module Title	Microprocessor Systems		
Degree/Diploma	Bachelor of Engineering Degree		
Type of Module	Major Option		
Modular Credits	4	Total student Workload	8 hours/week
		Contact hours	4 hours/week
Prerequisite	None		
Anti-requisite	None		
Aims			
<p>This module provides an introduction to microprocessor architecture, assembly language programming and the use of microcontrollers in implementing embedded systems. The internal architecture and operation of stored program microprocessors are examined in detail including arithmetic and logic units, special and general purpose registers, memory addressing modes and interrupts. Assembly programming techniques include data structures, branching, loops and subroutines are presented using simple design examples. Hardware and software techniques for I/O interfaces, both polled and interrupt driven are covered.</p>			
Learning Outcomes			
<i>On successful completion of this module, a student will be expected to be able to:</i>			
Lower order:	30%	<ul style="list-style-type: none"> - describe the functions and inter-relate the different microprocessor types - understand the functions of microprocessor components 	
Middle order :	30%	<ul style="list-style-type: none"> - identify hardware limitations for a given network configuration - investigate different problems and solutions that may be encountered for different microprocessor architectures 	
Higher order:	40%	<ul style="list-style-type: none"> - develop system requirements from a given user requirements - design, plan and justify suitable microprocessor to achieve a given objective - discuss the advantages/disadvantages of different options that are available 	
Module Contents			
<ul style="list-style-type: none"> - The architecture and organization of a microcontroller - Structured, well-commented, understandable programs in assembly and in a higher-level language - Assembly language programs involving I/O devices - Programs to perform I/O and interrupts - Concepts and principles of system buses in the design of bus control logic and I/O interfaces using LSI supporting devices - Memory modules using ROM and RAM devices 			
Assessment	Formative assessment	Quizzes will be used to test and to give feedback for their learning	
	Summative assessment	Examination: 40% Coursework: 60% <ul style="list-style-type: none"> - 2 class tests (10% each) - 2 assignments (20% each) 	